

Mb - Megabit

Used to define real and virtual storage - Equal to 1,048,576 bits

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NS - Nanosecond

one billionth of a second.

PARITY BIT

a bit added to a group of bits to make the total number of 1's either even or odd. When recalculated by computer after transmission or storage of data, a change from even to odd or odd to even indicates an error.

PC100 SDRAM Memory

Intel Specification for SDRAM memory devices and modules that will reliably operate at a 100MHz Front Side Bus (FSB) frequency. PC-100 SDRAM memory is backward compatible to systems that use PC-66 SDRAM memory.

PC133 SDRAM Memory

Intel Specification for SDRAM memory devices and modules that will reliably operate at a 133MHz Front Side Bus (FSB) frequency. PC-133 SDRAM memory is backward compatible to systems that use either PC-100 or PC-66 SDRAM memory.

PCMCIA

Personal Computer Memory Card International Association. Group that sets the "industry standard" for credit card style devices.

QUAD-CAS

uses single x4 parity chips for each group of 8 DRAM chips on a 72-pin SIMM.

RAM - Random Access Memory

system memory in which programs and data are stored temporarily while the computer is operating. The more RAM your computer has, the more data it can store at one time and subsequently the more efficient your computer will operate.

RAS

Row Address Strobe - memory controller signal that tells the memory that it can read the row address signal.

SIMM

Single In-line Memory Module - consists of multiple DRAM chips mounted on a single circuit board for easy installation.